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	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CYPR-CD01207M	

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/JP/	11	Walters; "Computer-Aided Prototyping For ASIC-Based Systems"; 1991; IEEE Design and Test of Computers	<input type="checkbox"/>
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/JP/	22	"In-Circuit Emulators- descriptions of the major ICEs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm	<input type="checkbox"/>
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/JP/	24	CYPR-CD00182; "In-System Chip Emulator Architecture"; October 10, 2001; 09/975,115; Snyder et al	<input type="checkbox"/>

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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Amir A. Tabarrok/	Date (YYYY-MM-DD)	2008-05-27
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